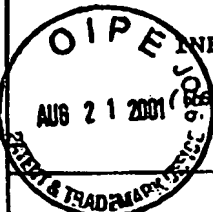


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## INFORMATION DISCLOSURE STATEMENT

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Eliyahou Harari et al.FILED  
May 30, 2001GROUP  
2824

## U. S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
AT	A5	4 5 9 9 7 0 7	07/1986	Fang			
	A6	4 7 1 8 0 4 1	01/1988	Baglee et al.			
	A7	4 7 6 3 3 0 5	08/1988	Kuo			
	A8	4 8 0 5 1 5 1	02/1989	Terada et al.			
	A9	4 8 1 1 2 9 4	03/1989	Kobayashi et al.			
	A10	5 0 5 3 9 9 0	10/1991	Kreifels et al.			
AT	A11	5 0 9 5 3 4 4	03/1992	Harari			

## FOREIGN PATENT DOCUMENTS

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AT	B3	6 2 2 5 7 6 9 9	11/1987	Japan			Abstr

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AT	C8	"PLL TV Microcomputer Interface," SGS-ATES Componenti Electronici SpA, MOS Integrated Circuits/M290, 15 pps. (June, 1982)
	C9	Gee et al., "An Enhanced 16K E <sup>2</sup> PROM," <u>IEEE Journal of Solid State Circuits</u> , Vol. SC-17, No. 5 (Oct. 1982)
	C10	Torelli, "An LSI technology fully compatible EAROM cell," <u>SGS-ATES Componenti Electronici SpA</u> , 7pp. (1982)
	C11	Caironi et al., "A TV Frequency Synthesis System With A Single Chip Microprocessor Interface Including Non Volatile Memory," <u>IEEE Transactions on Consumer Electronics</u> , Vol. CE-28, No. 3, pp. 363-71 (Aug. 1982)
AT	C12	Torelli et al., "Integrating Non-Volatile Station Memory and Remote Control Receiver In A Single Chip," <u>IEEE Digest of Technical Papers</u> , Cat. No. 83CH1872-1, 2pp. (June, 1983)

EXAMINER

/Andrew Tran/

DATE CONSIDERED

04/27/2006

\* EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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